

CERTIFICATE OF FACSIMILE

I hereby certify that this correspondence is being faxed to Examiner John J. Tabone, Jr. (703-872-9306) at the USPTO, on June 21, 2005


Irena Nikolova

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In Re Application of:

Date: June 21, 2005

Robert T. BAILIS, et al.

Confirmation No: 5286

JUN 21 2005

Serial No: 10/016,449

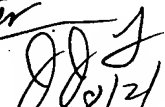
Group Art Unit: 2133

Filed: December 10, 2001

Examiner: John J. Tabone, Jr.

For: METHOD AND SYSTEM FOR USE OF A FIELD PROGRAMMABLE GATE
ARRAY (FPGA) FUNCTION WITHIN AN APPLICATION SPECIFIC
INTEGRATED CIRCUIT (ASIC) TO ENABLE CREATION OF A DEBUGGER
CLIENT WITHIN THE ASIC

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AMENDMENT IN REPLY TO ACTION OF MARCH 23, 2005

In response to the Office Action dated March 23, 2005, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 6 of this paper.